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In re Application of:

Roland Ochoa et al.

Serial No.: Not Yet Assigned

Filed: August 27, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING COMPRESSION CIRCUITRY FOR COMPRESSING TEST DATA, AND THE TEST SYSTEM AND METHOD FOR UTILIZING THE SEMICONDUCTOR INTEGRATED CIRCUIT

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 3011.3US (92-657.4)

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

The present application is a continuation of application Serial No. 09/175,518, filed October 20, 1998, pending, which is a continuation of application Serial No. 08/881,946, filed June 25, 1997, now U.S. Patent 5,864,565, issued January 26, 1999, which is a continuation of application Serial No. 08/353,404, filed Dec. 9, 1994, now abandoned, which is a continuation-in-part of application Serial No. 08/077,182, filed June 15, 1993, now abandoned.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior applications, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior applications is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: August 27, 2001

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Enclosure: Form PTO-1449

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